

Customer No.: 31561  
Application No.: 10/708,371  
Docket NO.: 11955-US-PA

### **REMARKS**

#### **Present Status of the Application**

Claims 1-15 are pending.

Applicants have amended claims 1, 7, and 13 and add new claims 16 and 17. Support for the changes can be found in the drawings, the disclosure, and the originally filed claims. Specifically, Applicants submit that as originally filed, the power amplifier with an active bias circuit comprises "a voltage source". It is well known for those of ordinary skill in the art that an active device is a device that requires a source of energy for its operation, in such a manner, as also fully supported by Figs. 3 and 7; the active bias circuit comprises a voltage source and a grounded level, and the output bias voltage is powered by the voltage source. Entering of the amendments is respectfully solicited.

#### **Discussion of Rejections to The Claims Under 35 U.S.C. 102(b)**

The Office Action rejected claims 1-15 under 35 U.S.C. 102, as being anticipated by Ishikawa et al. (US 5982236, hereinafter referred as Ishikawa).

In response thereto, Applicants have amended claims 1 and 7, and hereby otherwise traverse these rejections. As such, Applicants submit that claims 1 and 7 are neither taught, disclosed, nor suggested by Ishikawa, or any of the other cited

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references, taken alone or in combination, and thus should be allowed.

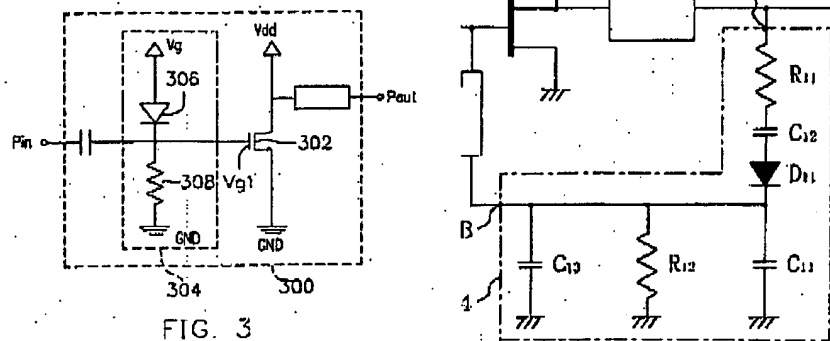
With respect to claim 1, independent claim 1 recites the features as follows:

1. (currently amended) A power amplifier with an active bias circuit, comprising:

... wherein the active bias circuit comprises a voltage source ...  
 (emphasis added)

Ishikawa et al. does not teach the claimed limitation of "wherein the active bias circuit comprises a voltage source", as required by claim 1, as currently amended.

It is more apparent when comparing the drawings of the cited references with the drawing of the current application, on which the claims relies.



The above left figure, illustrating the claimed invention, shows that the active bias circuit 304 includes a voltage source Vg and a grounded level GND, while the positive voltage generation circuit 4 of Ishikawa illustrated in above right figure is a

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passive device without a power source. Therefore, Applicants submit that the present invention, as set forth in claim 1, as currently amended, is novel and unobvious over Ishikawa, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Regarding claim 7, as the same reasons stated above, the Ishikawa reference at least does not disclose "wherein the active bias circuit comprises a voltage source" as set forth in claim 7, as currently amended. Thus, the Ishikawa reference does not anticipate claim 7, and the rejection should be withdrawn.

Furthermore, Applicants submit that Ishikawa fails to teach, disclose or suggest "a power input device connected to an output terminal of the power amplifier transistor for receiving an amplified output power from the power amplifier transistor" as required by claim 7, as currently amended.

In rejecting claim 7 and addressing this subject matter, the Examiner designates the input terminal of 3 of Ishikawa. However, in the claimed invention, the power device, the active bias circuit and the power amplifier are connected in serial, as described in the claims ("an active bias circuit connected to the power output device and the gate of the power amplifier transistor" as claimed) and in the specification. The power device is not connected to the output of the power amplifier, as the device 3 in Fig. 1 of '236. And these three devices are not connected as a circle.

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Regarding claim 13, as the similar reasons stated above, the Ishikawa reference at least does not disclose "wherein the gate bias voltage is powered by a voltage source other than the input power" as set forth in claim 13, as currently amended. Thus, the Ishikawa reference does not anticipate claim 13, and the rejection should be withdrawn.

If independent claims 1, 7 and 13 are allowable over the prior art of record, then its dependent claims 2-6, 8-12 and 14-15 are allowable as a matter of law, because these dependent claims contain all features of their respective independent claim 1, 7 and 13.

#### **New Claims**

Claims 16 and 17 are newly added, respectively depending on allowable independent claims 1 and 7, and thus should also be allowable.

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### CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1-15 and new claims 16 and 17 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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